



**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Michael B. Ball

**Serial No.:** 09/943,880

**Filed:** August 30, 2001

**For:** METHOD OF FABRICATION OF  
STACKED SEMICONDUCTOR DEVICES

**Confirmation No.:** 2391

**Examiner:** A. Chambliss

**Group Art Unit:** 2827

**Attorney Docket No.:** 2269-2769.6US  
(95-1118.04/US)

**Notice of Allowance Mailed:**

December 31, 2003

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

March 16, 2004  
Date

Signature

Deidra J. Pfeil

Name (Type/Print)

**AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

**Amendments to the Specification** begin on page 3 of this paper.

**Serial No. 09/943,880**

**Amendments to the Claims** are reflected in the listing of claims which begins on page 10 of this paper.

**Remarks** begin on page 13 of this paper.